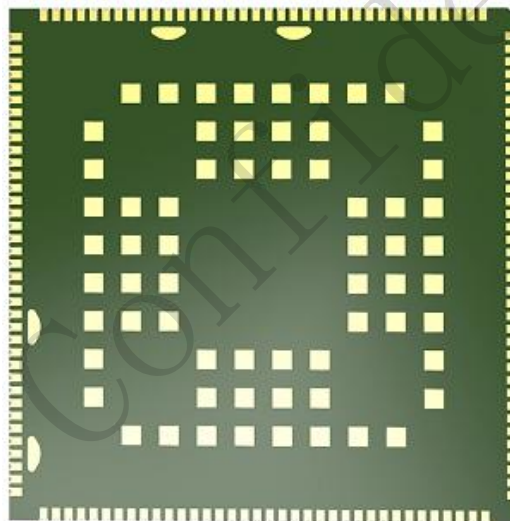


SLM755L Module Datasheet_V1.01



File name	SLM755L Module Datasheet
Version	V1.01
Company	Shenzhen MeiG Smart Technology Co.,Ltd
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1.Product Overview

SLM755L, based on Qualcomm platform of MSM8909, is a new generation of LTE smart communication module. It supports multi-modes of network connectivity including TD-LTE/FDD-LTE/WCDMA/TD-SCDMA/EVDO/CDMA/GSM and supports multi-modes of satellite signals of GPS/Glonass/Beidou for positioning/location. SLM755L has a memory of 8GB+1GB (compatible with 16GB+2GB) with built-in Android OS. It is integrated with a high-performance GPU-Adreno 304 and features encoding and decoding of various videos and audios, besides it supports 720P video, has a rich set of interfaces for video and audio and abundant GPIO ports.

SLM755L not only provides high-speed access of broadband data, but also has the functions of voice, texts, address books, wifi, bluetooth and GPS. This module is applicable in a wide range of applications, such as video monitoring, security, vehicle equipment, smart hand-held terminals etc.

2. Feature list

Features	Descriptions
Platform	Qualcomm MSM8909
CPU	Quad-core A7 (32bit) 1.1GHz
GPU	A304 409.6MHz
System memory	8GB eMMC + 1GB LPDDR3 *Optional 16GB+2GB
OS	Android6.0
Size	40.5x40.5x3.0mm, LCC+LGA LGA 210pin
Network Band (Chinese Bands)	TDD-LTE: B38/39/40/41 FDD-LTE: B1/3/5/8 TD-SCDMA: B34/39 WCDMA: B1/B5/B8 EVDO: BC0 CDMA: BC0 GSM: B5/3/8/2
Wi-Fi	IEEE 802.11b/g/n 2.4G

Bluetooth		BT4.0	
FM		Available	
GNSS		GPS/Beidou/Glonass	
Data transmission	TDD-LTE	Cat4 TDD-LTE 117/30Mbps	
	FDD-LTE	Cat4 FDD-LTE 150/50Mbps	
	DC-HSPA+	42/11.2Mbps	
	TD-HSPA	2.8/2.3Mbps	
	EVDO Rev.A	3.1/1.8Mbps	
	EDGE	Class12, 236.8kbps/236.8kbps	
	GPRS	Class12, 85.6kbps/85.6kbps	
SIM		DSDS (Dual cards dual standby) 3.0/1.8V Support SIM hot plug L/W/G+G with CSFB to W/G L/TDS/G+G with CSFB to TDS/G L/EVDO/CDMA1x+G Don't support dual CDMA sim-cards	
Display/Panel		Matrix: HD(720p): 1280*720@60fps	
		LCM Size: User defined	
		Interface: MIPI DSI 4-lane	
Camera (Front and rear)		Interface: main: MIPI CSI0 2-lanes; front: MIPI CSI1 1-lanes	
		Camera Pixel: Max: Front 2M/Rear 8M	
		Video decode	1080p 30 fps: HEVC/H264/ MP4/DivX/VP8 WVGA 30 fps:H.263
		Video encode	720p 30 fps:H264 WVGA 30 fps:VP8/MP4
Input Device		Keys (on-off key、volume key)	
		Capacitive TP	
Reset		Support HW reset	
Interfaces		Interface	Main function description
		VBAT	Power input, 3.3V~4.2V, normal: 3.8V
		SDIO *1	TF Card, Maximum support:32GB
		USB	Support OTG FORCE_USB_BOOT (For emergency download)
		UART*2	4 line UART (RX, TX, CTS, RTS), 2 line UART(RX,TX)

	I2C*4	For sensors/TP/others
	SPI*1	Master only
	ADC*2	Support
	Charging	Charging IC
	Motor	Support
	GPIO	24
	VCOIN	Real-time clock
	RF interface	LCC interface
	Audio	Two for single-ended MIC (ECM&MEMS) One is for headphone MIC One is for speaker (amplifier available) One is for receiver One is for stereo headphones

3. SLM755L Interfaces

SLM755L module 210pin definitions:

pin#	pin name	Pad characteristics	Functional description
1	VBAT	PI,PO	Battery
2	VBAT	PI,PO	Battery
3	GND	GND	GND
4	MIC_IN1_P	AI	Microphone 1 input, single-ended
5	GND_MIC	GND	MIC GND
6	MIC_IN2_P	AI	Earphone Microphone input, single-ended
7	GND	GND	GND
8	CDC_EAR_P	AO	Earpiece amplifier output, differential plus
9	CDC_EAR_M	AO	Earpiece amplifier output, differential minus
10	SPKR_OUT_P	AO	Speaker (0.85w / 4.2V) driver output, plus
11	SPKR_OUT_M	AO	Speaker (0.85w / 4.2V) driver output, minus
12	GND	GND	GND
13	USB_DM	AI, AO	USB data minus
14	USB_DP	AI, AO	USB data plus
15	GND	GND	GND
16	USB_ID	AI	USB ID
17	UIM2_DET_N	DI,B-PD:nppukp	Configurable I/O UIM2 removal detection
18	UIM2_RESET	DO,B-PD:nppukp	Configurable I/O UIM2 reset
19	UIM2_CLK	DO,B-PD:nppukp	Configurable I/O UIM2 clock
20	UIM2_DATA	B,B-PD:nppukp	Configurable I/O UIM2 data
21	VREG_L15_UIM2	PO	PMIC supply for UIM2
22	UIM1_DET_N	DI,B-PD:nppukp	Configurable I/O UIM1 removal detection
23	UIM1_RESET	DO,B-PD:nppukp	Configurable I/O UIM1 reset
24	UIM1_CLK	DO,B-PD:nppukp	Configurable I/O UIM1 clock
25	UIM1_DATA	B,B-PD:nppukp	Configurable I/O UIM1 data
26	VREG_L14_UIM1	PO	PMIC supply for UIM1
27	GND	GND	GND
28	VIB_DRV_N	PI	Vibration motor driver output control
29	PM8909_MPP2	AO-Z; DO	Configurable MPP; used for PWM
30	TP_INT_N	DI;B-PD:nppukp	Configurable I/O Touchscreen interrupt
31	TP_RST_N	DI;B-PD:nppukp	Configurable I/O Touchscreen reset
32	VREG_L12_SDC	PO	PMU supply 2.95V
33	GPIO23	B-PD:nppukp	Configurable I/O
34	UART1_TX	B-PD:nppukp	Configurable I/O UART
35	UART1_RX	B-PD:nppukp	Configurable I/O UART
36	UART1_CTS	B-PD:nppukp	Configurable I/O UARTor I2C SDA

37	UART1_RTS	B;B-PD:nppukp	Configurable I/O UARTor I2C SCL
38	VREG_L11_SDC	PO	PMIC output 2.95V
39	SDC2_SDCARD_CLK	BH-NP:pdpu kp	Secure digital controller 2 clock
40	SDC2_SDCARD_CMD	BH-PD:nppukp	Secure digital controller 2 command
41	SDC2_SDCARD_D0	BH-PD:nppukp	Secure digital controller 2 data bit 0
42	SDC2_SDCARD_D1	BH-PD:nppukp	Secure digital controller 2 data bit 1
43	SDC2_SDCARD_D2	BH-PD:nppukp	Secure digital controller 2 data bit 2
44	SDC2_SDCARD_D3	BH-PD:nppukp	Secure digital controller 2 data bit 3
45	SDC2_SDCARD_DET	B-PD:nppukp	Configurable I/O ,SD_DET_N
46	FORCE_USB_BOOT	B-PD:nppukp	pullup to 1.8v will force MSM to boot from USB_HS port
47	TP_I2C_SCL	B;B-PD:nppukp	Configurable I/O CTP I2C
48	TP_I2C_SDA	B;B-PD:nppukp	Configurable I/O CTP I2C
49	LCD_RST_N	B-PD:nppukp	Configurable I/O, #DSI_RST#,MDP_VSYNC_S
50	LCD_TE0	B-PD:nppukp	Configurable I/O,CCI_TIMER0, GP_CLK0
51	GND	GND	GND
52	MIPI_DSI0_CLK_N	AO	MIPI display serial interface 0 clock – negative
53	MIPI_DSI0_CLK_P	AO	MIPI display serial interface 0 clock –positive
54	MIPI_DSI0_LANE0_N	AI, AO	MIPI display serial interface 0 lane 0 – negative
55	MIPI_DSI0_LANE0_P	AI, AO	MIPI display serial interface 0 lane 0 –positive
56	MIPI_DSI0_LANE1_N	AI, AO	MIPI display serial interface 0 lane 1 – negative
57	MIPI_DSI0_LANE1_P	AI, AO	MIPI display serial interface 0 lane 1 –positive
58	MIPI_DSI0_LANE2_N	AI, AO	MIPI display serial interface 0 lane 2 –negative
59	MIPI_DSI0_LANE2_P	AI, AO	MIPI display serial interface 0 lane 2 –positive
60	MIPI_DSI0_LANE3_N	AI, AO	MIPI display serial interface 0 lane 3 –negative
61	MIPI_DSI0_LANE3_P	AI, AO	MIPI display serial interface 0 lane 3 –positive
62	GND	GND	GND
63	MIPI_CSI0_CLK_N	AI	MIPI camera serial interface 0 CLK – negative
64	MIPI_CSI0_CLK_P	AI	MIPI camera serial interface 0 CLK –positive
65	MIPI_CSI0_LANE0_N	AI, AO	MIPI camera serial interface 0 lane 0 – negative
66	MIPI_CSI0_LANE0_P	AI, AO	MIPI camera serial interface 0 lane 0 – positive
67	MIPI_CSI0_LANE1_N	AI, AO	MIPI camera serial interface 0 lane 1 – negative
68	MIPI_CSI0_LANE1_P	AI, AO	MIPI camera serial interface 0 lane 1 – positive
69	GND	GND	GND
70	MIPI_CSI1_CLK_N	AI	MIPI camera serial interface 1 clock –negative
71	MIPI_CSI1_CLK_P	AI	MIPI camera serial interface 1 clock – positive
72	MIPI_CSI1_LANE0_N	AI, AO	MIPI camera serial interface 1 lane 0–negative
73	MIPI_CSI1_LANE0_P	AI, AO	MIPI camera serial interface 1 lane 0 – positive
74	CAM0_MCLK	DO;B-PD:nppukp	Camera master clock 0
75	CAM1_MCLK	DO;B-PD:nppukp	Camera master clock 1 Configurable I/O
76	GND	GND	GND
77	RF_WIFI/BT	AI,AO	RF signal

78	GND	GND	GND
79	CAM0_RST_N	DO;B-PD:nppukp	Rear camera reset; Configurable I/O
80	CAM0_PWDN	DO;B-PD:nppukp	Rear camera pwn Configurable I/O
81	CAM1_RST_N	DO;B-PD:nppukp	Front camera reset Configurable I/O
82	CAM1_PWDN	DI;B-PD:nppukp	Front camera pwn Configurable I/O
83	CAM_I2C_SCL	B-PD:nppukp	Camera I2C_SCL,can't be used for other
84	CAM_I2C_SDA	B-PD:nppukp	Camera I2C_SDA,can't be used for other
85	GND	GND	GND
86	GND	GND	GND
87	RF_MAIN	AI,AO	RF signal-Main ANT
88	GND	GND	GND
89	GND	GND	GND
90	GPIO32	B-PD:nppukp	Configurable I/O
91	GPIO7_I2C_SCL	DO;B-PD:nppukp	Configurable I/O I2C or GPIO
92	GPIO6_I2C_SDA	DO;B-PD:nppukp	Configurable I/O I2C or GPIO
93	UART2_MSM_RX	B;B-PD:nppukp	Configurable I/O UART for debug
94	UART2_MSM_TX	BD;B-PD:nppukp	Configurable I/O UART for debug
95	GPIO90_KEY_VOL_UP	DI;B-PD:nppukp	Configurable I/O Keypad sense bit 0
96	GPIO91_KEY_VOL_DOWN	DI;B-PD:nppukp	Configurable I/O Keypad sense bit 1
97	GPIO31	B-PD:nppukp	Configurable I/O
98	GPIO92	B-PD:nppukp	Configurable I/O
99	GPIO88	B-PD:nppukp	Configurable I/O
100	GPIO89	B-PD:nppukp	Configurable I/O
101	GPIO69	B-PD:nppukp	Configurable I/O
102	GPIO68	B-PD:nppukp	Configurable I/O
103	GPIO97	B-PD:nppukp	Configurable I/O
104	GPIO110	B-PD:nppukp	Configurable I/O
105	GPIO0	B-PD:nppukp	Configurable I/O
106	GPIO98	B-PD:nppukp	Configurable I/O
107	GPIO94	B-PD:nppukp	Configurable I/O
108	GPIO36	B-PD:nppukp	Configurable I/O
109	GPIO65	B-PD:nppukp	Configurable I/O
110	GPIO96	B-PD:nppukp	Configurable I/O
111	VREG_L5_1P8	OUPUT	PMU Supply 1.8V
112	GPIO58	B-PD:nppukp	Configurable I/O
113	GPIO99	B-PD:nppukp	Configurable I/O
114	KYPD_PWR_N	DI	Power on key
115	GPIO95	B-PD:nppukp	Configurable I/O
116	GPIO11_SPI_CLK	B-PD:nppukp	Configurable I/O SPI or I2C
117	GPIO10_SPI_CS	B-PD:nppukp	Configurable I/O SPI or I2C
118	GPIO9_SPI_MISO	B-PD:nppukp	Configurable I/O SPI

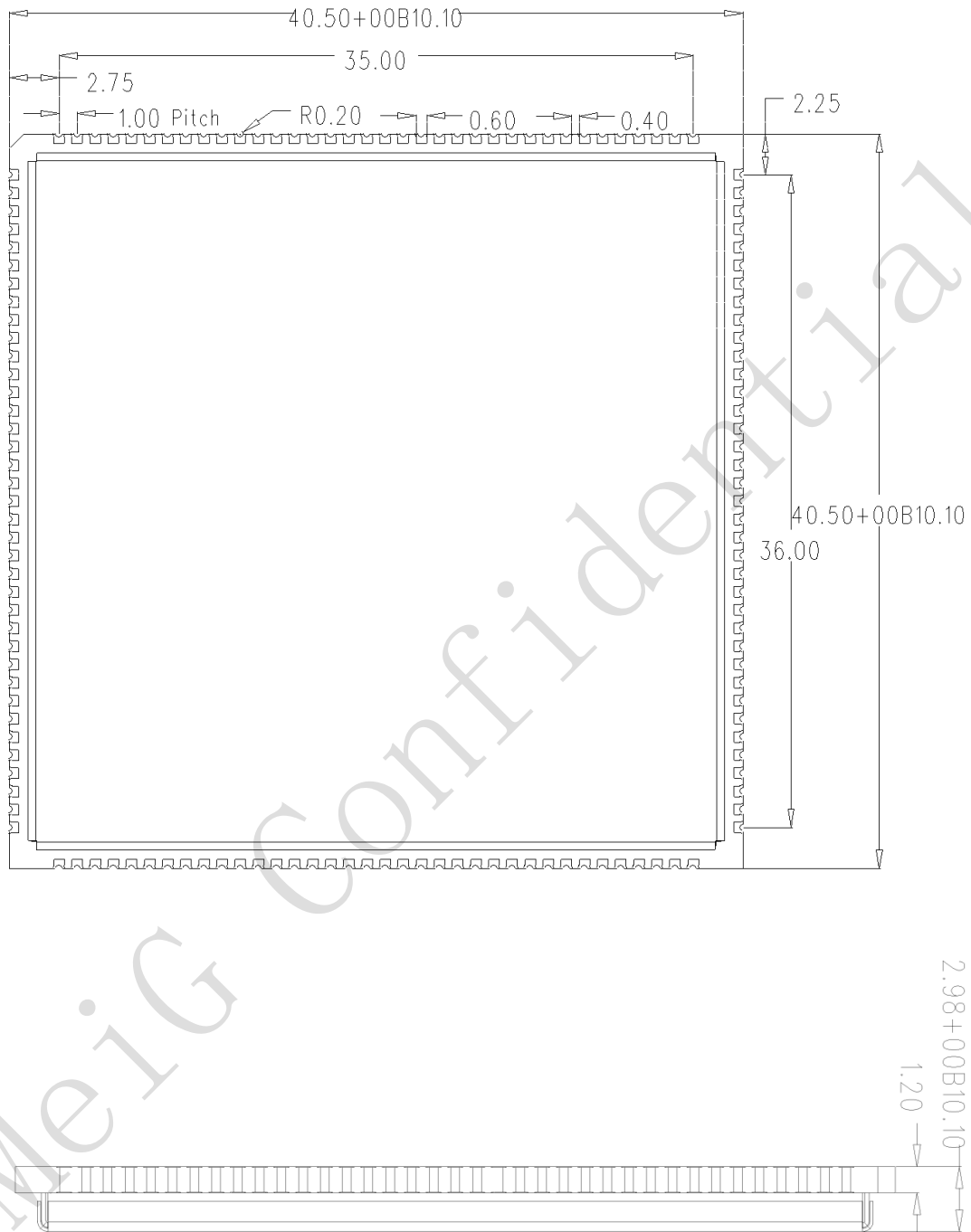
119	GPIO8_SPI_MOSI	B-PD:nppukp	Configurable I/O SPI
120	GND	GND	GND
121	RF_GPS	AI	RF signal-GPS ANT
122	GND	GND	GND
123	GPIO16	B-PD:nppukp	Configurable I/O,
124	GPIO17	B-PD:nppukp	Configurable I/O,
125	VREG_L6_1P8	OUPUT	PMU Supply 1.8V
126	VCOIN	PI	VCOIN
127	CHARGE_SEL	DI	SMB CHAGER SEL
128	PM8909_MPP4	AO-Z;DI	Configurable MPP; used for ADC IN
129	VREG_L17_2P85	OUPUT	PMU Supply 2.85V
130	GND	GND	GND
131	RF_DIV	AI	RF signal-DIV ANT
132	GND	GND	GND
133	VBATT_SNS	PI	Battery voltage sense point
134	BAT_THERM	DI	Battery therm monitor
135	GND	GND	GND
136	CDC_HPH_R	AO	Earphone right output
137	CDC_HPH_REF	AI	Earphone driver amplifier ground reference
138	CDC_HPH_L	AO	Earphone left output
139	CDC_HS_DET	DI	Headset detection
140	GND	GND	GND
141	VBUS	PI	USB VBUS Voltage
142	VBUS	PI	USB VBUS Voltage
143	GND	GND	GND
144	GND	GND	GND
145	VBAT	PI,PO	Battery
146	VBAT	PI,PO	Battery
147	GND	GND	GND
148	GND	GND	GND
149	GND	GND	GND
150	GND	GND	GND
151	MIC3_IN3_P		Microphone 3 input, single-ended
152	MIC_BIAS1		MIC1 bias power supply
153	MIC_BIAS2		MIC2 bias power supply
154	RESERVED		RESERVED
155	RESERVED		RESERVED
156	RESERVED		RESERVED
157	RESERVED		RESERVED
158	RESERVED		RESERVED
159	RESERVED		RESERVED
160	GND	GND	GND

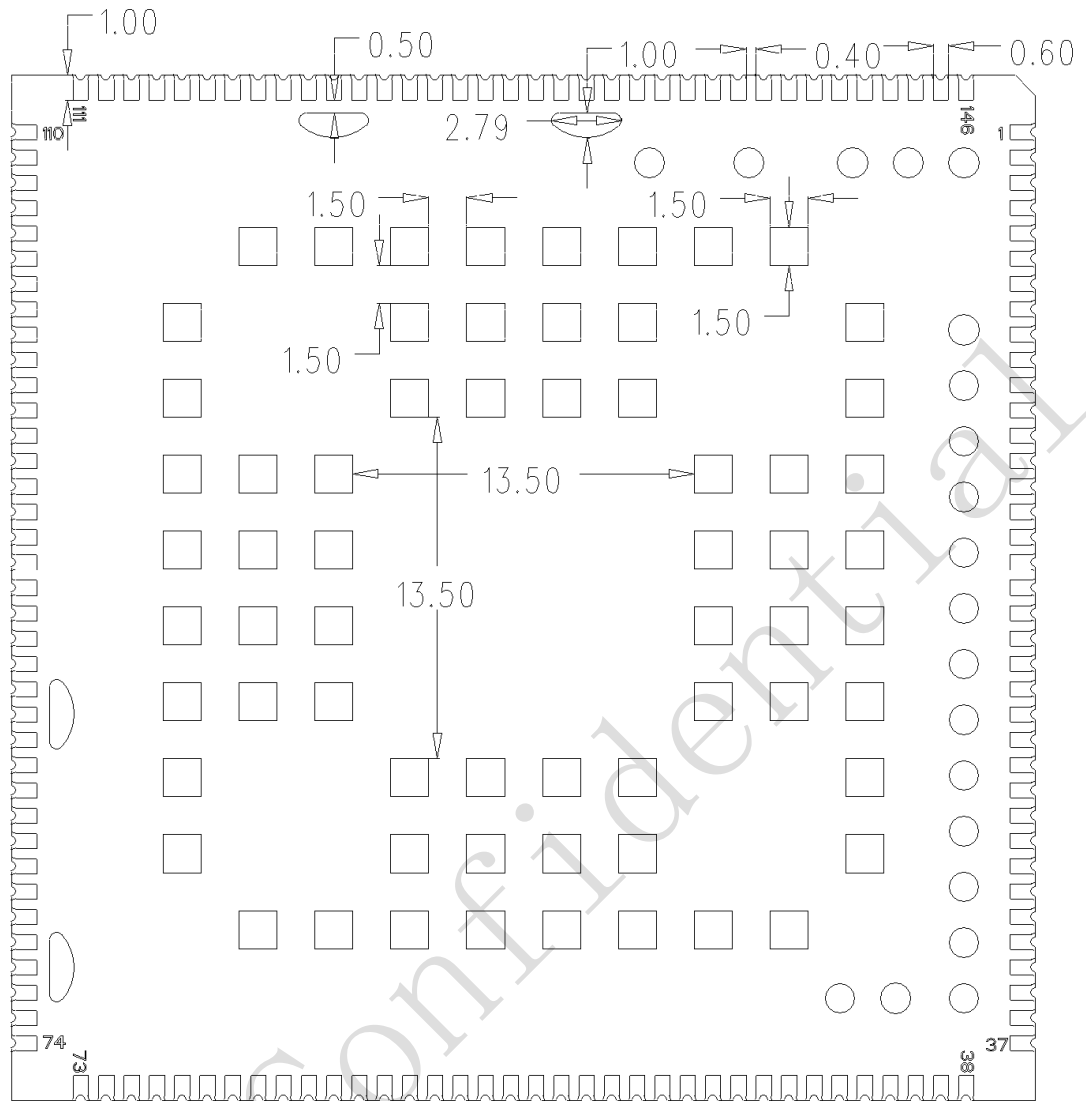
161	GND	GND	GND
162	GND	GND	GND
163	GND	GND	GND
164	GND	GND	GND
165	GND	GND	GND
166	GND	GND	GND
167	GND	GND	GND
168	GND	GND	GND
169	GND	GND	GND
170	GND	GND	GND
171	GND	GND	GND
172	GND	GND	GND
173	GND	GND	GND
174	GND	GND	GND
175	GND	GND	GND
176	GND	GND	GND
177	GND	GND	GND
178	GND	GND	GND
179	PM_RESIN_N	DI	PMIC reset
180	GND	GND	GND
181	GND	GND	GND
182	GND	GND	GND
183	RESERVED		RESERVED
184	GND	GND	GND
185	GND	GND	GND
186	GND	GND	GND
187	RESERVED		RESERVED
188	GND	GND	GND
189	GND	GND	GND
190	RF_FM	AO	RF signal-FM ANT
191	RESERVED		RESERVED
192	GND	GND	GND
193	GND	GND	GND
194	RESERVED		RESERVED
195	RESERVED		RESERVED
196	RESERVED		RESERVED
197	RESERVED		RESERVED
198	GND	GND	GND
199	RESERVED		RESERVED
200	GND	GND	GND
201	GND	GND	GND
202	RESERVED		RESERVED

203	RESERVED		RESERVED
204	RESERVED		RESERVED
205	RESERVED		RESERVED
206	RESERVED		RESERVED
207	CHG_LED_SINK		Negative Power charging indicator isink
208	GND	GND	GND
209	GND	GND	GND
210	CBL_PWR_N	DI	Cable poweron detect input

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4. 2D Diagram of Module





5. PIN Assignment

